


[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)
Search: The ACM Digital Library The Guide

non blocking transaction single synchroniz*



Searching within **The ACM Digital Library** for: non blocking transaction single synchroniz* (start Found 198 of 263,029)

REFINE YOUR SEARCH

▼ Refine by Keywords

non blocking transac



Discovered Terms

▼ Refine by People

Names

Institutions

Authors

Reviewers

▼ Refine by publications

Publication Year

Publication Names

ACM Publications

All Publications

Content Formats

Publishers

▼ Refine by Conferences

Sponsors

Events

Proceeding Series

ADVANCED SEARCH

Advanced Search

FEEDBACK

Please provide us with feedback

Found 198 of 263,029

Search Results

Results 1 - 20 of 198

Related Journals**Related Magazines****Related SI**Sort by

Save results to a Binder

Result page: 1

1 Transactional events

Kevin Donnelly, Matthew Fluet

September 2006 **ICFP '06: Proceedings of the eleventh ACM SIGPLAN international conference on functional programming****Publisher:** ACM

Full text available: Pdf (264.80 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 43, Downloads (Ever): 100

Concurrent programs require high-level abstractions in order to manage reasoning. In this paper, we introduce a novel concurrency abstraction, combines first-class synchronous messagepassing ...

Keywords: concurrency, first-class events, monads, synchronous messagepassing, functional programming, parallel computation

Also published in:

September 2006 **SIGPLAN Notices** Volume 41 Issue 9**2 Deadlock detection in distributed object systems**

Nima Kaveh, Wolfgang Emmerich

September 2001 **ESEC/ FSE-9: Proceedings of the 8th European software engineering conference and the 9th ACM SIGSOFT international symposium on Foundations of software engineering****Publisher:** ACM

Full text available: Pdf (198.67 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 37, Downloads (Ever): 100

The behaviour of a distributed system is largely determined by the use of policies of the underlying middleware. The inherent parallel nature of distributed systems leads to many interesting problems, such as deadlocks ...

Keywords: UML, model checking, object middleware, process algebra, distributed systems, deadlock detection

Also published in:

September 2001 **SIGSOFT Software Engineering Notes** Volume 26 Issue 5**3 Efficient fork-linearizable access to untrusted shared memory**

Christian Cachin, Abhi Shelat, Alexander Shraer

August 2007 **PODC '07: Proceedings of the twenty-sixth annual ACM symposium on Principles of distributed computing**

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (359.46 KB)

Additional Information: full citation, abst

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 91, Download Link

When data is stored on a faulty server that is accessed concurrently by inconsistent data to different clients. For example, the server might respond with stale data ...

Keywords: arbitrary failures, fork-consistency, storage emulations

4 Checkpoint and Restart for Distributed Components in XCAT

Sriram Krishnan, Dennis Gannon

November 2004 **GRID '04: Proceedings of the 5th IEEE/ACM International Conference on Grid Computing**

Publisher: IEEE Computer Society

Full text available:  Pdf (160.71 KB)

Additional Information: full citation, abst

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 22, Download Link

With the advent of Grid computing, more and more high-end computation is being done by distributed components. This opens up new avenues for scientific research, it makes the system a non-trivial ...

Keywords: Grids, Components, Web Services, Distributed Checkpointer

5 Using elimination to implement scalable and lock-free FIFO queues

 Mark Moir, Daniel Nussbaum, Ori Shalev, Nir Shavit

July 2005 **SPAA '05: Proceedings of the seventeenth annual ACM symposium on Parallel algorithms and architectures**

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (196.23 KB)

Additional Information: full citation, abst

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 89, Download Link

This paper shows for the first time that elimination, a scaling technique for data structures, can be applied to FIFO data structures, specifically, to linear transform existing nonscalable ...

Keywords: FIFO queues, elimination, linearizability, lock-free, multiprocessor scalability

6 Lazy argument passing in Java RMI

 Christopher Line, K. R. Jayaram, Patrick Eugster

September 2008 **PPSJ '08: Proceedings of the 6th international symposium on Java**

Publisher: ACM

Full text available:  Pdf (846.57 KB)

Additional Information: full citation, abst

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 67, Download Link

Though often criticized for its inherent synchronization overhead and co

paradigm remains one of the most popular abstractions for building distributed systems ...

Keywords: Java, argument passing, asynchronous, future, invocation,

7 Inferential queueing and speculative push for reducing critical section contention

Ravi Rajwar, Alain Kagi, James R. Goodman
June 2003 ICS '03: Proceedings of the 17th annual international conference

Publisher: ACM Request Permissions

Full text available: Pdf (568.93 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 28, Download

Communication latencies within critical sections constitute a major bottleneck in system workloads. In this paper, we argue for the use of Inferentially Queued Lock synchronization but also ...

Keywords: data forwarding, inferential queueing, synchronization

8 Architectural Support for Software Transactional Memory

Bratin Saha, Ali-Reza Adl-Tabatabai, Quinn Jacobson

December 2006 MICRO 39: Proceedings of the 39th Annual IEEE/ACM International Conference

Publisher: IEEE Computer Society

Full text available: Pdf (325.24 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 18, Downloads (12 Months): 180, Download

Transactional memory provides a concurrency control mechanism that eliminates the need for explicit synchronization. Researchers have proposed several different implementation techniques, classified into software transactional ...

9 Design tradeoffs in modern software transactional memory systems

Virendra J. Marathe, William N. Scherer, Michael L. Scott
October 2004 LCR '04: Proceedings of the 7th workshop on Workshop on Isolation and consistency for scalable systems

Publisher: ACM

Full text available: Pdf (158.62 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 53, Download

Software Transactional Memory (STM) is a generic non-blocking synchronization mechanism that converts the conversion of correct sequential objects into correct concurrent objects. It achieves traditional performance and ...

10 Software transactional memory for multicore embedded systems

Jennifer Mankin, David Kaeli, John Ardin
June 2009 LCTES '09: Proceedings of the 2009 ACM SIGPLAN/SIGBED conference on tools for embedded systems

Publisher: ACM Request Permissions

Full text available: Pdf (443.92 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 41, Downloads (12 Months): 167, Download

Embedded systems, like general-purpose systems, can benefit from par platform. Unfortunately, concurrency issues present in general-purpose systems, protection from which is currently ...

Keywords: embedded systems, locking, multicore, software transactional transactions

Also published in:

June 2009 **SIGPLAN Notices** Volume 44 Issue 7

11 Constant-RMR Implementations of CAS and other synchronization primitives

Wojciech Golić, Vassos Hadjicostis, Danny Hendler, Philipp Woelfel

August 2007 **PODC '07: Proceedings of the twenty-sixth annual ACM symposium on Principles of distributed computing**

Publisher: ACM Request Permissions

Full text available: Pdf (289.54 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 31, Download links

We consider asynchronous multiprocessors where processes communicate via memory. We show how to implement consensus, all comparison primitives, linked/store-conditional using only a constant ...

Keywords: comparison primitives, consensus, mutual exclusion, remote memory access

12 Performance Evaluation of Task Pools Based on Hardware Synchronization

Ralf Hoffmann, Matthias Korch, Thomas Rauber

November 2004 **SC '04: Proceedings of the 2004 ACM/IEEE conference on Supercomputing**

Publisher: IEEE Computer Society

Full text available: Pdf (208.41 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 35, Download links

A task-based execution provides a universal approach to dynamic load t are arbitrary units of work that are created dynamically at run-time and the task pool, until ...

13 Robust implicit EDF: A wireless MAC protocol for collaborative real-time systems

Tanya L. Crenshaw, Spencer Hoke, Ajay Tirimala, Marco Caccamo

September 2007 **Transactions on Embedded Computing Systems (TEC)**

Publisher: ACM Request Permissions

Full text available: Pdf (663.47 KB)

Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 19, Downloads (12 Months): 111, Download links

Advances in wireless technology have brought us closer to extensive de systems connected through a wireless channel. The medium-access con providing a real-time guarantee. ...

Keywords: Earliest deadline first, medium-access control, real time, wireless communication

 Deriving linearizable fine-grained concurrent objects

Martin Vechev, Eran Yahav

June 2008 **PLDI '08: Proceedings of the 2008 ACM SIGPLAN conference on implementation**

Publisher: ACM 

Full text available:  Pdf (935.82 KB)

Additional Information: [full citation](#), [abst](#)

Bibliometrics: Downloads (6 Weeks): 13, Downloads (12 Months): 140, Download

Practical and efficient algorithms for concurrent data structures are difficult. The literature are often optimized for a specific setting, making it hard to implement ...

Keywords: algorithms, concurrency, data structures, linearizability, memory

Also published in:

May 2008 **SIGPLAN Notices** Volume 43 Issue 6

15 Aggressive snoop reduction for synchronized producer-consumer co

 embedded multi-processors

Chenjie Yu, Peter Peter Petrov

September 2007 **CODES+ISSS '07: Proceedings of the 5th IEEE/ACM international conference on codesign and system synthesis**

Publisher: ACM

Full text available:  Pdf (163.62 KB)

Additional Information: [full citation](#), [abst](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 41, Download

Snoop-based cache coherence protocols are typically used when multiple common bus. It is well known, however, that these coherence protocols help alleviate this problem, ...

Keywords: cache coherence, embedded systems, low-power multiprocessors

16 Nonblocking k-compare-single-swap

 Victor Luchangco, Mark Moir, Nir Shavit

June 2003 **SPAA '03: Proceedings of the fifteenth annual ACM symposium on parallel algorithms and architectures**

Publisher: ACM 

Full text available:  Pdf (197.17 KB)

Additional Information: [full citation](#), [abst](#)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 41, Download

The current literature offers two extremes of nonblocking software synchronization: structure design: intricate designs of specific structures based on single-swap (CAS), and general-purpose ...

Keywords: concurrent data structures, linked lists, multiprocessors, no freedom

17 Practice of parallelizing network applications on multi-core architec

Junchang Wang, Haipeng Cheng, Bei Hua, Xianan Tang

 June 2009 **ICS '09**: Proceedings of the 23rd international conference on
Publisher: ACM 
 Full text available:  Pdf (1.05 MB) Additional Information: full citation, abst:
Bibliometrics: Downloads (6 Weeks): 58, Downloads (12 Months): 154, Downl

The industry wide shift to multi-core architectures arouses great interes
 However, it is very difficult to parallelize fine-grained applications for m
 hardware support ...

Keywords: application-level protocol processing, deep content inspecti
 parallelization, pipelining implementation, tcp/ip protocol processing

18 Parallelizing security checks on commodity hardware

 Edmund B. Nightingale, Daniel Peek, Peter M. Chen, Jason Flinn
 March 2008 **ASPLOS XIII**: Proceedings of the 13th international conferen
 programming languages and operating systems

Publisher: ACM 

Full text available:  Pdf (26:0 MIN),  Pdf (306.87 KB) Additional Information: full citatio

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 194, Downl

Speck (Speculative Parallel Check) is a system that accelerates powerful
 executing them in parallel on multiple cores. Speck provides an infrastr
 particular ...

Keywords: operating systems, parallel, performance, security, specula

Also published in:

March 2008 **SIGPLAN Notices** Volume 43 Issue 3

March 2008 **SIGARCH Computer Architecture News** Volume 36 Issue 1

March 2008 **SIGOPS Operating Systems Review** Volume 42 Issue 2

19 Data sharing protocols for SMT processors

 Shih-Wu Lo
 April 2006 **SAC '06**: Proceedings of the 2006 ACM symposium on Applied
 Publisher: ACM 

Full text available:  Pdf (197.60 KB) Additional Information: full citation, abst:

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 18, Downloa

Although there are many real-time task synchronization protocols design
 systems, most of them do not fit the needs in accommodating *simultaneou*
 synchronization protocols are ...

Keywords: real-time, scheduling, simultaneously multithreading

20 Improving instruction cache performance in OLTP

 Stavros Harizopoulos, Anastassia Ailamaki
 September 2006 **Transactions on Database Systems (TODS)**, Volume 31

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf \(783.16 KB\)](#)

Additional Information: [full citation](#), [abst](#)

Bibliometrics: Downloads (6 Weeks): 13, Downloads (12 Months): 105, Downl

Instruction-cache misses account for up to 40% of execution time in database workloads. In contrast to data cache misses, instruction misses are due to execution. Chip design limitations ...

Keywords: Instruction cache, cache misses

Result page: 1

The ACM Portal is published by the Association for Computing Machinery. Copyright ©
[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)